

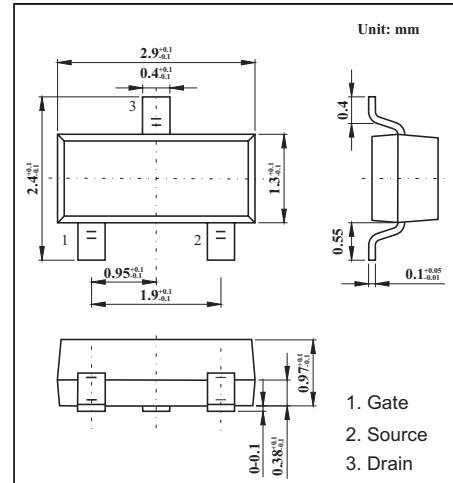
## SOT-23 Plastic-Encapsulate MOSFETs

### Features

- P-Channel -60V (D-S) MOSFE
- T • TrenchFET Power MOSFET

### MECHANICAL DATA

- Case style:SOT-23molded plastic
- Mounting position:any



### MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-source voltage	V <sub>DS</sub>	-60	V
Gate-source voltage	V <sub>GS</sub>	±20	V
Continuous drain current (T <sub>J</sub> = 150°C ) *1,2 T <sub>A</sub> =25 °C T <sub>A</sub> =100°C	I <sub>D</sub>	-1.25 -0.85	A
Pulsed drain current	I <sub>DM</sub>	-8	A
Avalanche Current L = 0.1 mH	I <sub>AS</sub>	-5	A
Maximum Power dissipation *1,2 T <sub>A</sub> =25 °C T <sub>A</sub> =70 °C	P <sub>D</sub>	1.25 0.8	W
Operating junction and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	°C

\*1 Surface Mounted on FR4 Board.

\*2 t≤5 sec

Thermal Resistance Ratings Ta = 25 °C

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction to Ambient* t≤5 s e c	R <sub>thJA</sub>	100 130	166	°C/W
Steady State				
Maximum Junction-to-Lead*	R <sub>thJL</sub>	45	60	

\* Surface Mounted on FR4 Board.

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V , I <sub>D</sub> = -250 μA	-60			V
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-1			
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V , V <sub>GS</sub> = ± 20 V			±100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -4.8 V , V <sub>GS</sub> = 0 V		-1		μ A
		V <sub>DS</sub> = -48V, V <sub>GS</sub> = 0 V , T <sub>J</sub> = 125 °C		-50		
On-state drain current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ -4.5 V , V <sub>GS</sub> = -10 V	-6			A
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V , I <sub>D</sub> = -1.25 A	0.275	0.340		Ω
		V <sub>GS</sub> = -4.5 V , I <sub>D</sub> = -1 A	0.406	0.550		
Forward transconductance	g <sub>f</sub>	V <sub>DS</sub> = -4.5 V , I <sub>D</sub> = -1 A		1.9		S
Total gate charge *	Q <sub>g</sub>			5.4	12	
Gate-source charge *	Q <sub>gs</sub>	V <sub>DS</sub> = -30 V , V <sub>GS</sub> = -10 V , I <sub>D</sub> = -1.25 A		1.15		nC
Gate-drain charge *	Q <sub>gd</sub>			0.92		
Turn-On Delay Time	t <sub>d(on)</sub>			10.5	20	
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = -30 V , R <sub>L</sub> = 30 Ω I <sub>D</sub> = -1 A , V <sub>GEN</sub> = -4.5V , R <sub>G</sub> = 6 Ω		11.5	20	ns
Turn-Off Delay Time	t <sub>d(off)</sub>			15.5	30	
Fall Time	t <sub>f</sub>			7.5	15	
Continuous Current	I <sub>s</sub>				-1.25	A
Pulsed Current	I <sub>SM</sub>				-8	A
Diode Forward Voltage*	V <sub>SD</sub>	I <sub>s</sub> = -1.25 A, V <sub>GS</sub> = 0 V		-0.82	-1.2	V
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>f</sub> = -1.25 A, dI/dt = 100 A / s μ		30	55	ns

\* Pulse test: PW ≤ 300 μ s duty cycle ≤ 2%.